● General Description

The AGM325ME combines advanced trenchMOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$.

This device is ideal for load switch and battery protection applications.

● Features

- ■ Advance high cell density Trench technology
- ■Low $R_{DS(ON)}$ to minimize conductive loss
- ■Low Gate Charge for fast switching
- ■Low Thermal resistance

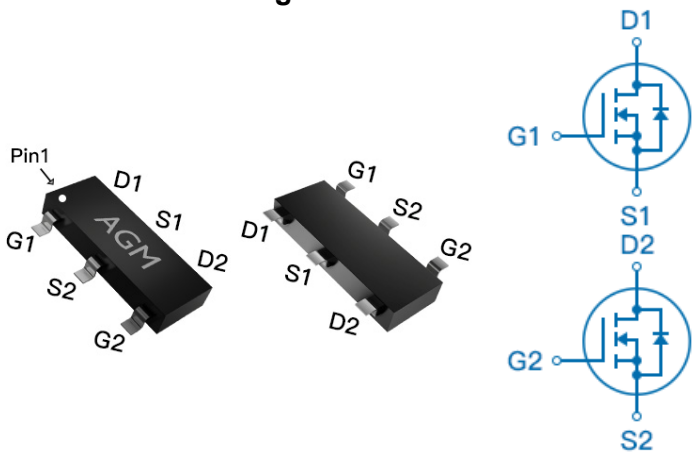
• Application

- ■MB/VGA Vcore
- ■SMPS 2nd Synchronous Rectifier
- ■POL application
- ■BLDC Motor driver

Product Summary

BVDSS	RDSON	ID
30V	17mΩ	5.5A

SOT23-6L Pin Configuration



Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
AGM325ME	AGM325ME	SOT23-6L	178mm	8mm	3000

Table 1. Absolute Maximum Ratings (T_A=25°C)

Symbol	Parameter	Value	Unit
V_{DS}	Drain-Source Voltage (V_{GS}=0V)	30	V
V_{GS}	Gate-Source Voltage (V_{DS}=0V)	±20	V
I_D	Drain Current-Continuous(T_A=25℃) (Note 1)	5.5	A
	Drain Current-Continuous(TA=70℃)	3.0	A
IDM (pluse)	Drain Current-Pulsed (Note 2)	22	A
P_D	Total Power Dissipation(TA=25℃)	1.25	W
TJ,TSTG	Operating Junction and Storage Temperature Range	-55 To 150	℃

Table 2. Thermal Characteristic

Symbol	Parameter	Typ	Max	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient (Steady State)[1]	---	100	℃/W

Table 3. N- Channel Electrical Characteristics (TJ=25℃unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BVDSS	Drain-Source Breakdown Voltage	VGS=0V ID=250μA	30	--	--	V
IDSS	Zero Gate Voltage Drain Current	VDS=30V,VGS=0V	--	--	1	μA
IGSS	Gate-Body Leakage Current	VGS=±20V,VDS=0V	--	--	±100	nA
VGS(th)	Gate Threshold Voltage	VDS=VGS,ID=250μA	1.2	--	2.2	V
gFS	Forward Transconductance	VDS=5V,ID=5A	--	7	--	S
RDS(on)	Drain-Source On-State Resistance	VGS=10V, ID=3.5A	--	17	35	mΩ
		VGS=4.5V, ID=3A	--	26	40	mΩ
Dynamic Characteristics						
Ciss	Input Capacitance	VDS=15V,VGS=0V, F=1MHZ	--	390	--	pF
Coss	Output Capacitance		--	54.5	--	pF
Crss	Reverse Transfer Capacitance		--	41	--	pF
Rg	Gate resistance	VGS=0V, VDS=0V,f=1.0MHz	--	3	--	Ω
Switching Times						
td(on)	Turn-on Delay Time	VGS=10V,VDS=15V, ID=4A,RGEN=6Ω	--	4	--	nS
tr	Turn-on Rise Time		--	2	--	nS
td(off)	Turn-Off Delay Time		--	22	--	nS
tf	Turn-Off Fall Time		--	3	--	nS
Qg	Total Gate Charge	VGS=10V, VDS=15V, ID=4A	--	4.5	--	nC
Qgs	Gate-Source Charge		--	1.4	--	nC
Qgd	Gate-Drain Charge		--	0.6	--	nC
Source-Drain Diode Characteristics						
ISD	Source-Drain Current(Body Diode)		--	--	5.5	A
VSD	Forward on Voltage	VGS=0V,IS=5A	--	0.8	1.2	V
trr	Reverse Recovery Time	IF=5A , dI/dt=100A/μs , TJ=25℃	--	11	--	ns
Qrr	Reverse Recovery Charge		--	5.5	--	nc

Notes 1.The maximum current rating is package limited.

Notes 2.Repetitive Rating: Pulse width limited by maximum junction temperature

Notes 3.EAS condition: TJ=25℃

N Channel Typical Characteristics

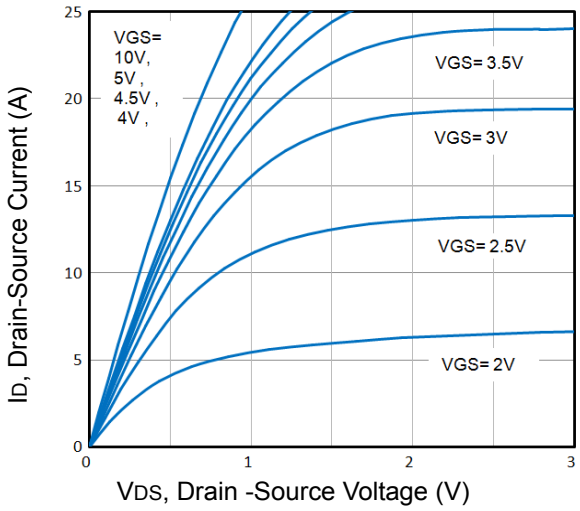


Fig1. Typical Output Characteristics

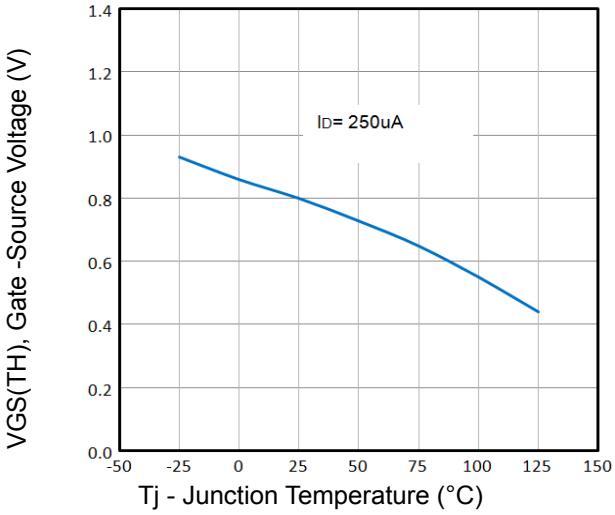


Fig2. Normalized Threshold Voltage Vs. Temperature

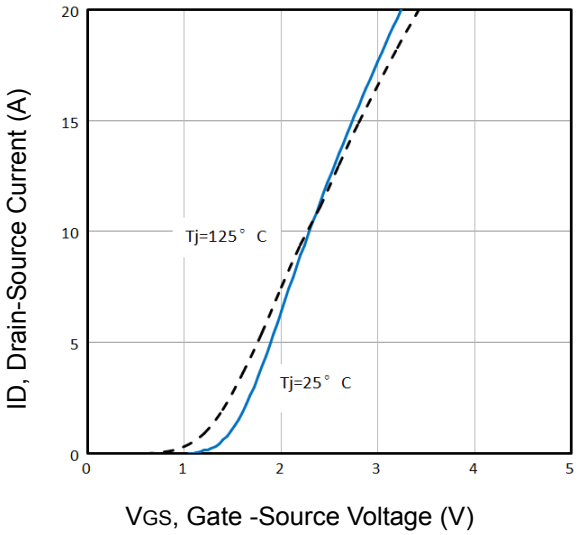


Fig3. Typical Transfer Characteristics

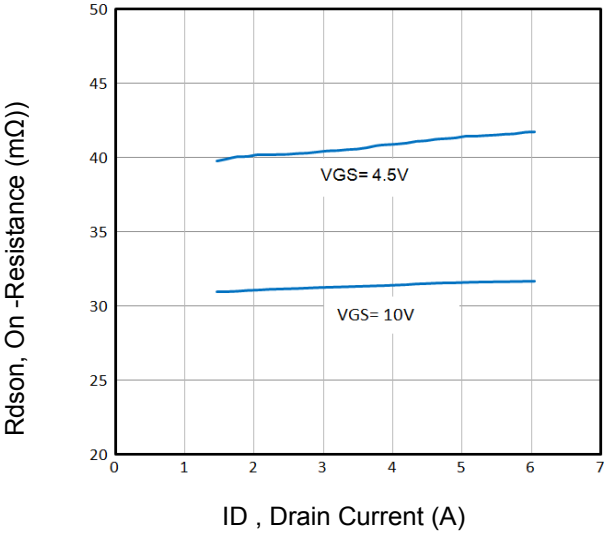


Fig4. On-Resistance vs. Drain Current and Gate

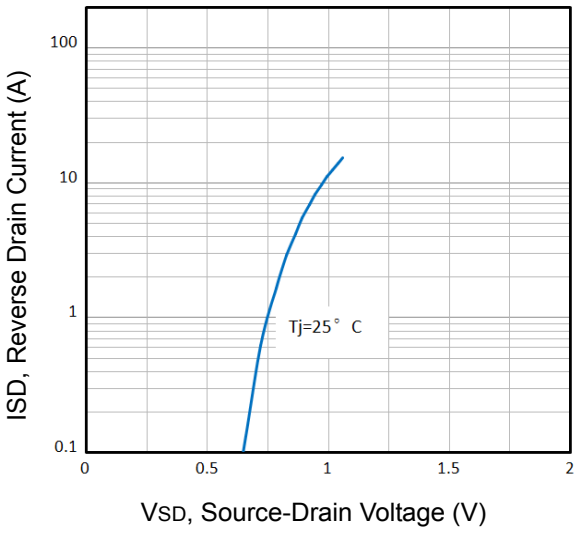


Fig5. Typical Source-Drain Diode Forward Voltage

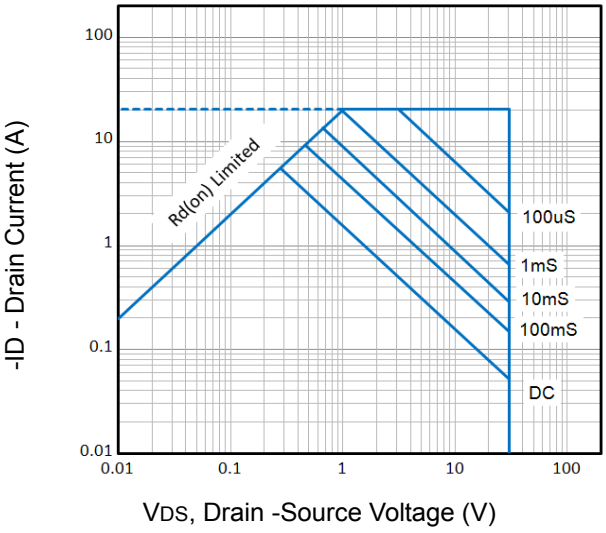


Fig6. Maximum Safe Operating Area

N Channel Typical Characteristics

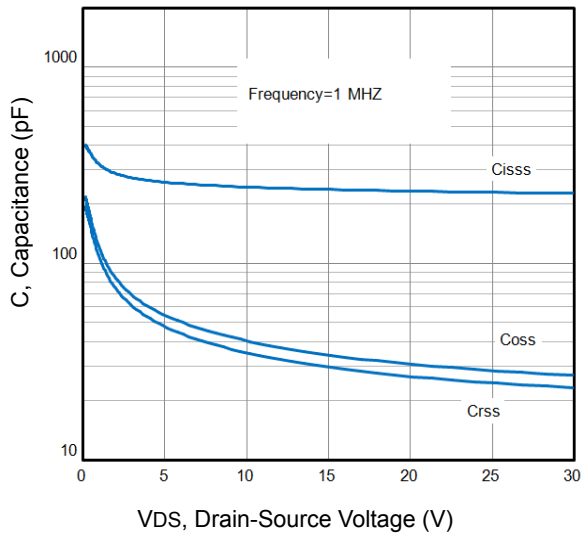


Fig7. Typical Capacitance Vs. Drain-Source Voltage

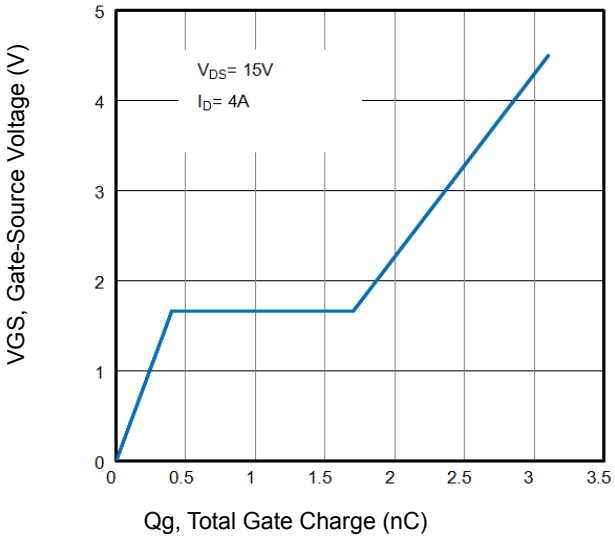


Fig8. Typical Gate Charge Vs. Gate-Source Voltage

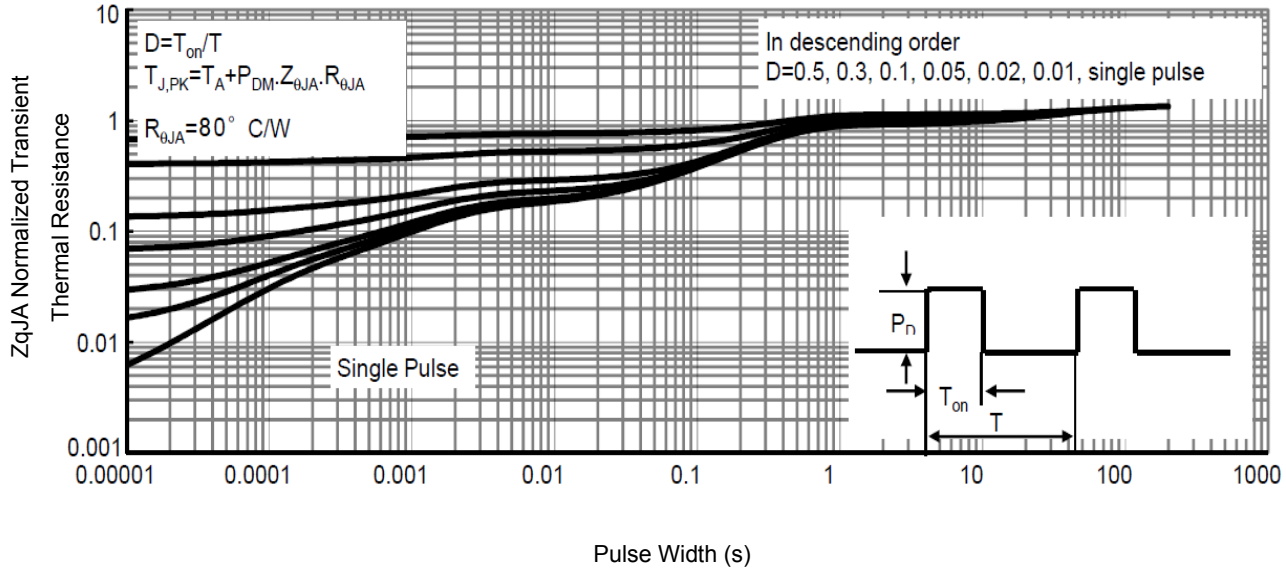


Fig9. Normalized Maximum Transient Thermal Impedance

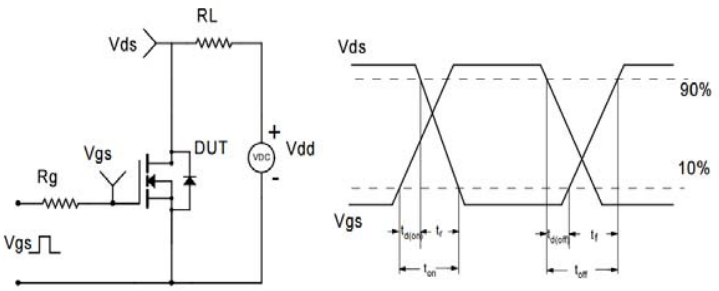
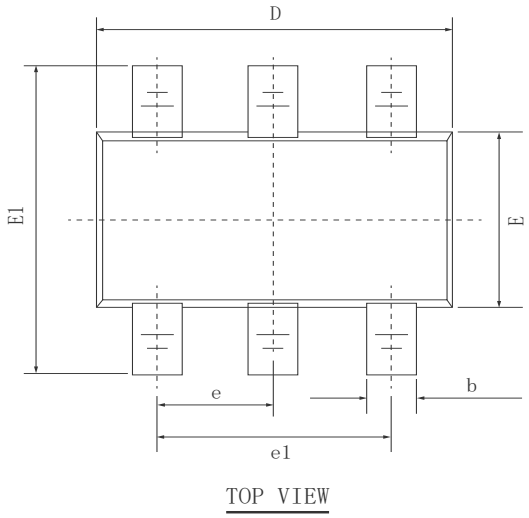
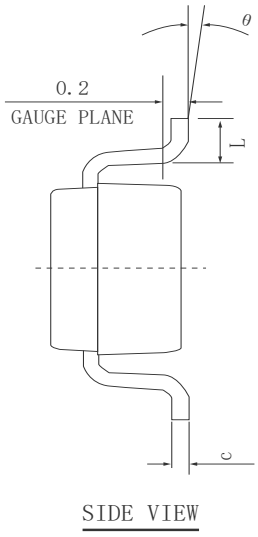


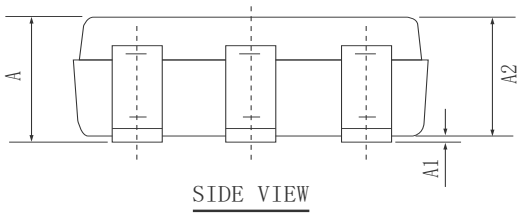
Fig10. Switching Time Test Circuit and waveforms

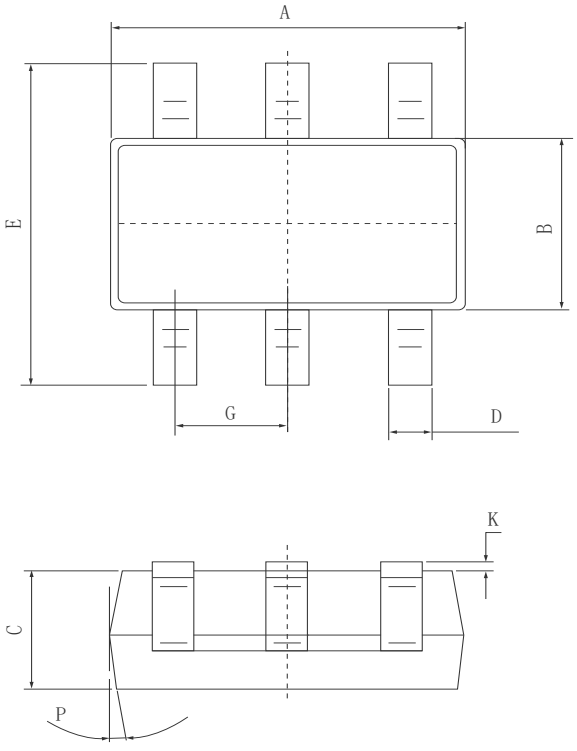
●Dimensions（SOT23-6L）

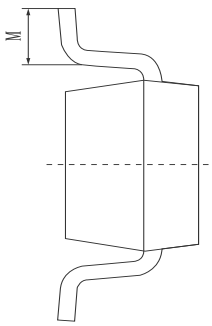




SYMBOL	MIN	NOM	MAX
A	—	—	1.20
A1	0.00	0.05	0.10
A2	1.00	1.10	1.20
b	0.30	0.40	0.50
c	0.10	0.125	0.15
e1	1.80	1.90	2.00
D	2.80	2.90	3.00
E	1.50	1.60	1.70
E1	2.60	2.80	3.00
L	0.30	0.45	0.60
θ	0°	4°	8°
e	0.95BSC		

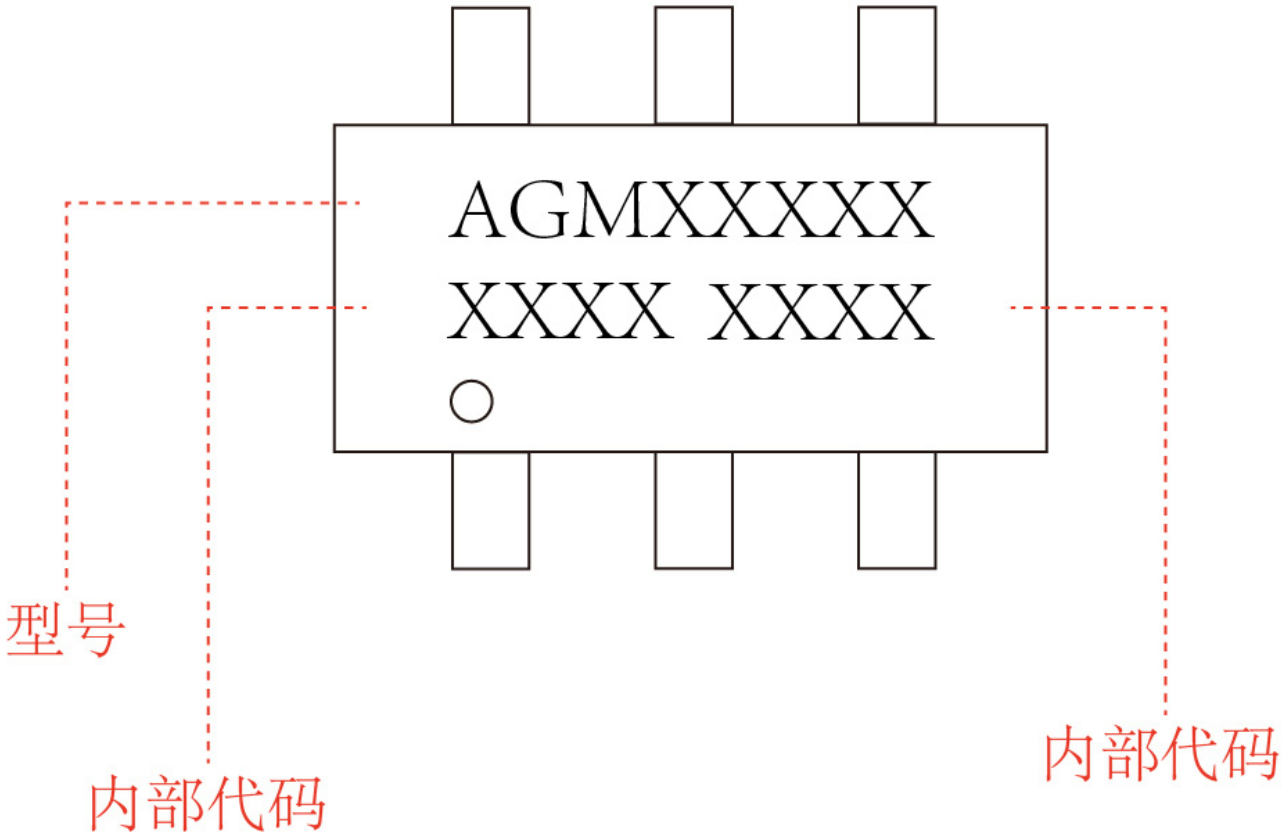






DIM	MILLIMETERS
A	2.82~3.02
B	1.60±0.10
C	1.10±0.05
D	0.40±0.10
E	2.65~2.95
G	0.95typ
K	0.00~0.10
M	0.20MIN
P	9±2°

SOT23-6L
Marking Instructions:



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